

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

1. (Currently amended) An amplifier module comprising:

an amplifier circuit for amplifying an input signal to generate an output signal; and

an adaptive bias circuit for receiving the input signal to provide a driving current to the amplifier circuit for controlling a quiescent current of the amplifier circuit,

wherein the adaptive bias circuit includes:

a driving transistor for receiving a driving transistor input current to provide the driving current to the amplifier circuit;

a drawing transistor for drawing a bypass current from the driving transistor input current to reduce the driving current in response to the input signal; and

an adjusting transistor for receiving the input signal to adjust a control voltage in response to the input signal,

wherein the drawing transistor is connected to the base of the driving transistor, and the bypass current increases when the control voltage increases.

2. (Cancelled)

3. (Currently amended) The amplifier module of claim 2 1, wherein the quiescent current is reduced when the driving current is reduced and the bypass current increases when the input signal is reduced.

4. (Cancelled)

5. (Cancelled)

6. (New) The amplifier module of claim 1, wherein the amplifier circuit includes a first transistor and a choke inductor.

7. (New) The amplifier module of claim 6, wherein the first transistor is of a multi-cell structure.

8. (New) The amplifier module of claim 6, wherein the choke inductor is an RF choke inductor.